

<b>Form PTO-1449</b> (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)		ATTY. DOCKET NO: 5707-04000		SERIAL NO: 10/789,903			
APPLICANT: McLeod et al.							
FILING DATE: February 27, 2004				GROUP: 2819			
<b>U.S. PATENT DOCUMENTS</b>							
EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
<b>FOREIGN PATENT DOCUMENTS</b>							
EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
JB	A1	Fredriksen, Einar; Tormod Njolstad, and Runar Sorasen, "Evaluation of algorithms for dynamic element matching in a Delta-Sigma A/D converter," undated, one page. (No date)					
JB	A2	Andersson, Niklas U., "A Comparison of Dynamic Element Matching in Digital-to-Analog Converters," undated, 17 pages. (No date)					
JB	A3	Andersson, Niklas U.; K. Ola Andersson, Mark Vesterbacka, and J. Jacob Wikner, "Models and Implementation of a Dynamic Element Matching DAC," Analog Integrated Circuits and Signal Processing, January 2003, Vol. 34, No. 1, pp. 5-6(2).					
JB	A4	Tuthill, Mike, "A Switched-Current, Switched-Capacitor Temperature Sensor in 0.6- $\mu$ m CMOS," IEEE Journal of Solid State Circuits, Vol. 33, No. 7, July 1998, pp.1117-1122.					
JB	A5	Rombouts, P.; L. Weyten, J. Raman, and S. Audenaert, "Capacitor Mismatch Compensation for the Quasi-Passive Switched-Capacitor DAC," IEEE Transactions on Circuits and Systems-I: Fundamental Theory and Applications, Vol. 45, No. 1, January 1998, pp.68-71.					
EXAMINER: <i>Glen Bruner</i>				DATE CONSIDERED: <i>1/27/05</i>			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.							